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radio, processor, and memory circuits. The RIC unit 9 preferably includes a transceiver 91, a microcontroller 93, a memory device 95, and a digital serial I/O port 97. The RIC unit 9 may be provided on a single CMOS chip and may include additional features such as a clock recovery system and a spread spectrum processor. The transceiver 91 may include, for example, a modulated back scatter transmitter. Of course, separate receivers and transmitters could be provided. An appropriate RIC unit is commercially available from Micron Communications, Inc. of Idaho through its MicroStamp™ product line. Accordingly, the details of the structure need not be further described. Additionally, further details of appropriate enabling circuitry for implementing the transceiver, processor and memory portions of Figure 2 are disclosed in commonly owned U.S. Patent No. 6,130,602, issued October 10, 2000, which is hereby incorporated by reference in its entirety.--

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Please replace the paragraph beginning on page 10, line 12 with the following rewritten paragraph:

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--In a preferred implementation, once the device is disabled, the microcontroller is programmed to maintain the transistor 21 in an OFF state until the device is reset by an authorized repair center or the rightful owner of the goods. For example, the microcontroller 93 can maintain the transistor 21 in the OFF state until an appropriate security code or other information that is available to the rightful owner (such as purchase date, location, etc.) is entered by way of input device 11. The microcontroller compares the input data to stored data to verify the information is being input by authorized personnel. If desired, provisions could also be made to reset the system remotely by